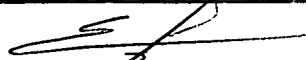


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		Application Number	10/766,801
		Filing Date	27 January 2004
		First Named Inventor	McCallister
		Art Unit	3261
		Examiner Name	Bayard
Sheet 2	of 2	Attorney Docket Number 2298-030	

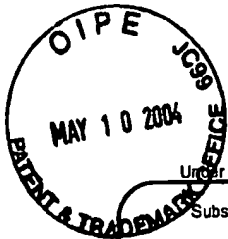
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Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
EB		PARK, WOO, RAICH, KENNEY, ZHOU, "Adaptive Predistortion Linearization of RF Power Amplifiers Using Lookup Tables Generated from Subsampled Data", IEEE 2002.	
		MORGAN, MA, "Reducing Measurement Noise Effects in Digital Predistortion of RF Power Amplifiers", Bell Laboratories, IEEE 2003.	
		SANTAMARIA, IBANEZ, LAZARO, PANTALEON, VIELVA, "Modeling Nonlinear Power Amplifiers in OFDM Systems from Subsampled Data: A Comparative Study Using Real Measurements" 2003.	
		IBANEZ-DIAZ, PANTALEON, SANTAMARIA, FERNANDEZ, MARTINEZ, "Nonlinear Estimation in Power Amplifiers Based on Subsampled Temporal Data", IEEE 2001.	
		ALLEN, BROWN, "Proceedings of the International Symposium on Advanced Radio Technologies", March 4-7, 2003, US Dept. of Commerce.	
EB		EUN, POWERS, "A New Volterra Predistortion Based on the Indirect Learning Architecture", 1997 IEEE.	

Examiner Signature		Date Considered	1/2/05
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Application Number	10/766,801
Filing Date	27 January 2004
First Named Inventor	Ron McCallister
Art Unit	
Examiner Name	
Attorney Docket Number	2298-030

Sheet 1 of 2**U. S. PATENT DOCUMENTS**

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
R/S		US- 2003/0227981	12/11/2003	Vella-Coleiro, et al	
		US- 2003/0076894	04/24/2003	Jin, et al.	
		US- 2002/0180523	12/05/2002	Okubo, et al.	
		US- 2004/0021517	02/05/2004	Irvine, et al.	
		US- 2004/0013206	01/22/2004	Gamm, et al.	
		US- 2003/0020538	01/30/2003	Kim	
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		US- 6,118,335	09/12/2000	Nielsen, et al.	
R/S		US- 5,923,712	07/13/1999	Leyendecker, et al.	
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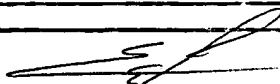
Application Number	10/766,801
Filing Date	27 January 2004
First Named Inventor	Ron D. McCallister
Art Unit	
Examiner Name	
Attorney Docket Number	2298-030

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ds/b		GLENTIS, KOUKOULAS, & KALOUPSIDIS "Efficient Algorithms for Volterra System Identification" IEEE Transactions on Signal Processing, Vol 47, No. 11, Nov 1999.	
		DING, RAICH, & ZHOU "A Hammerstein Predistortion Linearization Design Based on the Indirect Learning Architecture" Georgia Inst. of Tech. School of Elect. & Comp. Engineering.	
		RAZZAGHI & CHANG "A 10-b, 1GSample/s ADC using SiGe BiCMOS Technology" Electrical Engineering Department, UCLA.	
		DANESHRAD "DSP VLSI Engine for Electronic Linearization of Fiber Optic Links" Wireless Integrated Systems Research Group, UCLA, 02/27/2001.	
		FOX "Analysis and Dynamic Range Enhancement of the Analog-to-Digital Interface in Multimode Radio Receivers" Virginia Polytechnic Inst. & State Univ. 02/1997.	
ds/b		HUMMELS "Linearization of ADCS and DACS for All-Digital Wide-Bandwidth Receivers" Depart. of Electrical & Comp. Engineering, Univ. of Maine, 09/09/1999.	

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1/2/05

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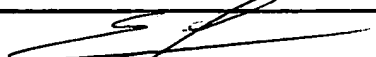
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)		Application Number	
		Filing Date	
		First Named Inventor	Ronald D McCallister
		Art Unit	
		Examiner Name	
Sheet <u>1</u> of <u>3</u>	Attorney Docket Number	2298-030	

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Examiner Initials*	Cite No. ¹	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
df		US- 5,778,029	07/07/1998	Kaufmann	
		US- 6,282,247	08/28/2001	Shen	
		US- 6,222,878	04/24/2001	McCallister, et al.	
		US- 6,512,800	01/28/2003	Amir, et al.	
		US- 2002/0193078A1	12/19/2002	Shearer, III, et al.	
		US- 5,721,756	02/24/1998	Liebetreu, et al.	
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		US- 6,442,217	08/27/2002	Cochran	
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		US- 2003/0063683A1	04/03/2003	Shearer, III, et al.	
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		US- 2003/0146787A1	08/07/2003	Hedberg, et al.	
		US- 2003/0156658A1	08/21/2003	Dartois	
		US- 2003/0123568A1	07/03/2003	Ophir, et al.	
		US- 2003/0179831A1	09/25/2003	Gupta, et al.	
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Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T ⁶
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Examiner Initials*	Cite No. ¹	Document Number <small>Number-Kind Code² (if known)</small>	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
2-15 ↑		US- 5,113,414	05/12/1992	Karam, et al.	
		US- 2002/0064236A1	05/30/2002	Matsuoka, et al.	
		US- 6,240,278	05/29/2001	Midya, et al.	
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		US- 4,878,029	10/31/1989	Saulnier, et al.	
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2-17		US- 4,290,139	09/15/1981	Walsh	
		US- 4,422,175	12/20/1983	Bingham, et al.	
		US- 4,633,482	12/30/1989	Sari	
		US- 4,053,837	10/11/1977	Ryan, et al.	
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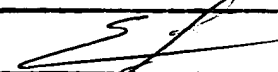
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		Applicant Number	
		Filing Date	
		First Named Inventor	Ronald D McCallister
		Art Unit	
		Examiner Name	
Sheet 3	of 3	Attorney Docket Number	2298-030

NON PATENT LITERATURE DOCUMENTS			
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ds		CAVERS, "The Effect of Quadrature Modulator and Demodulator Errors on Adaptive Digital Predistorters for Amplifier Linearization" IEEE Trans on Veh Tech, Vol46, #2, 05/97.	
dl		DING & ZHOU, "Effects of Even-order Nonlinear Terms on Power Amplifier Modeling and Predistortion Linearization" IEEE Transactions on Vehicular Technology, 08/2002	
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		OHLSSON, "Studies on Implementation of Digital Filters With High Throughput and Low Power Consumption" Institute of Technology, Dept of Electrical Engineering, P 61-73, 06/03.	
		RAICH, QIAN & ZHOU, "Digital Baseband Predistortion of Nonlinear Power Amplifiers Using Orthogonal Polynomials" Proc. ICASSP 2003) p689-692, Hong Kong China 04/2003.	
		GODAVARTI "Multiple Antennas in Wireless Communications" Array Signal Processing and Channel Capacity" University of Michigan, P 16-52, 2001.	
		"Pact Application Note: Adaptive Filter Based on the LMS-Algorithm"	
V		CHOO "Mapping LMS Adaptive Filter IP Core to Multiplier-Array FPGA Architecture for High Channel-Density VOIP Line Echo Cancellation" CMP United Business Media, 2003.	
ds		WIDROW, MCCOOL & BALL, "The Complex LMS Algorithm" Information Systems Laboratory, Naval Undersea Center, Fleet Engineering Dept, 08/19/1974.	

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